

EAST - [default.wsp:1]

File View Edit Tools Window Help

Drafts Pending Active Failed

DBs: USPAT:US-PPGPUB,EPO,JPO  
Default operator: OR  
 Bursts  Highlight all hi terms initially

Type L# Hits Search Text DBs

1	BRS	L1	1	(second near2 chip) same busy same ready same indicat\$3 same distinct	USPAT: US-PPGPUB: 20 EPO: JPO
2	BRS	L2	1	memory and ((second near2 chip) same busy same ready same indicat\$3 same distinct)	USPAT: US-PPGPUB: 20 EPO: JPO
3	BRS	L3	155	memory and (second near2 chip) and busy and ready and indicat\$3 and distinct	USPAT: US-PPGPUB: 20 EPO: JPO
4	BRS	L4	1	memory and (second near2 chip) and (busy same ready same indicat\$3 same distinct)	USPAT: US-PPGPUB: 20 EPO: JPO
5	BRS	L5	73	(memory same (second near2 chip)) and busy and ready and indicat\$3 and distinct	USPAT: US-PPGPUB: 20 EPO: JPO

Details HTML

Ready

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File View Edit Tools Window Help

Drafts Pending Active L1: (1) (second near3 chip) same (first near3 chip) L2: (37) ((second near3 chip) same (first near3 chip)) Failed Saved Favorites Tagged (3)

DBs: USPAT; US-PPGUB; EPO; JPO  
Default operator: OR  
P: Bursts  
P: Highlight all hit terms initially

BRS terms IS4K.htm Images Text HTML

Type	L#	Hits	Search Text	DBs
1	BRS	L1	1 (second near3 chip) same (first near3 chip) same enable same respons\$3 same ready same busy	USPAT; US-PPGUB; 20 EPO; JPO
2	BRS	L2	37 ((second near3 chip) same (first near3 chip)) and (enable same respons\$3) and (indicat\$3 same ready same busy)	USPAT; US-PPGUB; 20 EPO; JPO

Details HTML Ready NUM